

Notice of References Cited	Application/Control No. 09/583,386	Applicant(s)/Patent Under Reexamination CARLEY, L. RICHARD	
	Examiner Erik Kielin	Art Unit 2813	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,573,679	11-1996	Mitchell et al.	216/2
X	B	US-5,578,976	11-1996	Yao, Jun J.	200/184
	C	US-5,834,333	11-1998	Seefeldt et al.	438/409
X	D	US-5,493,177	02-1996	Muller et al.	313/578
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, et al. Silicon Processing for the VLSI Era, Vol. 1-Process Technology, Lattice Press: Sunset Beach CA, 1986, pp. 428-429.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.